

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

M-300 US

Sheet 1 of 1

SERIAL NO.

06/843,454

LIST OF RELEVANT ART CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Richard A. Blanchard

FILING DATE

03/24/86

GROUP

114

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
T.T	AA	4 4 5 4 6 4 6	6/19/84	Joy et al.	29	576 _A W	8/27/81
T.T	AB	4 4 5 4 6 4 7	6/19/84	Joy et al.	29	576 _A W	8/27/81
T	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
	AL	0 0 6 5 4 4 7	4-84	Japan	29	576 W	X
	AM						
	AN						
	AO						
	AP						

OTHER

ART (Including Author, Title, Date, Pertinent Pages, Etc.)

T.T	AR	Ammar et al., "UMOS TRANSISTORS ON (110) SILICON", IEEE Transactions on Electron Devices, Vol. ED-27, No. 5, May 1980, pgs. 907-914.
T.T	AS	Pshaenich, "MOS THYRISTOR IMPROVES POWER-SWITCHING CIRCUITS", Electronic Design, May 12, 1983, pgs. 165-170.
	AT	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.